

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

1-6. (Canceled)

1 7. (Currently amended): A color projection-type display system having a
2 convergence correction signal generation component comprising:
3 an analog signal generator ~~for producing~~which generates said convergence
4 correction signal, said analog signal generator having an input portion for receiving digital
5 convergence correction data; and
6 a memory coupled to supply digital data to said analog signal generator,
7 said memory configured with convergence correction data for a high definition
8 video signal which has a blanking period shorter than that of a NTSC signal, said convergence
9 correction data ~~comprising first data~~ corresponding to correction points along a horizontal scan
10 line,
11 said convergence correction data including additional correction points which are
12 set in proximity to an edge portion of a display area in said horizontal scan line, the number of
13 correction points in said convergence correction data being greater than the number of correction
14 points in convergence correction data for said NTSC signal,
15 ~~said correction points being unevenly distributed along said horizontal scan line,~~
16 wherein correction points including additional correction points proximate an said edge portion
17 of said display area horizontal scan line have smaller separation than corrections points in a
18 central portion of said display area, said convergence correction data being used for convergence
19 correction of the displayed high definition video signal horizontal scan line, so that the rate at
20 ~~which said convergence correction data is received by said analog signal generator varies when~~
21 ~~said television display displays a high definition video signal which has a blanking period shorter~~
22 ~~than that of a NTSC signal.~~

1 8. (Original): The convergence correction signal component of claim 7
2 wherein said analog signal generator includes a low-pass filter for producing said convergence
3 correction signal and a filter parameter selection circuit for selectively adjusting a parameter of
4 said low-pass filter depending on said convergence correction data.

1 9. (Currently amended): A color projection-type television system including
2 a convergence correction signal generating apparatus for correcting convergence in a display
3 area of said television system, said convergence correction signal generating apparatus
4 comprising:

5 a memory configured with convergence correction data for a high definition video
6 signal which has a blanking period shorter than that of a NTSC signal;

7 an address generation circuit operatively coupled to said memory to access said
8 convergence correction data; and

9 an analog signal generation circuit coupled to said memory to receive said
10 convergence correction data from said memory and operable to produce said convergence
11 correction signal from said convergence correction data,

12 said convergence correction data corresponding to correction points along a
13 horizontal scan line and including additional correction points which are set in proximity to an
14 edge portion of a display area in said horizontal scan line, the number of correction points in said
15 convergence correction data being greater than the number of correction points in convergence
16 correction data for said NTSC signal,

17 ~~said convergence correction data corresponding to correction points along a~~
18 ~~horizontal scan line, wherein separation between correction points located in a central portion of~~
19 ~~said display area is greater than separation between correction points including additional~~
20 ~~correction points located in an said edge portion, said convergence correction data being used for~~
21 ~~convergence correction of the displayed high definition video signal of said display area, wherein~~
22 ~~the rate at which said convergence correction data is received by said address generation circuit~~
23 ~~varies when said television display displays a high definition video signal which has a blanking~~
24 ~~period shorter than that of a NTSC signal.~~

1 10. (Original): The apparatus of claim 9 wherein said convergence correction
2 data is stored in said memory such that a first pair of adjacent convergence correction data is
3 separated by zero or more empty memory locations and a second pair of adjacent convergence
4 correction data is separated by zero or more empty memory locations, said first pair of adjacent
5 convergence correction data have a memory location spacing different from said second pair of
6 adjacent convergence correction data.

11-27. (Canceled)